

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,032,200 B1  
APPLICATION NO. : 10/657964  
DATED : April 18, 2006  
INVENTOR(S) : Sririam Satakopan et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

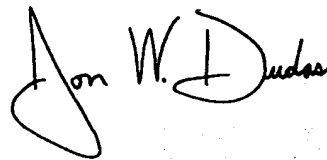
In column 4, Table 2, replace "Path cycle time without LV<sub>i</sub> device penalty" with --Path cycle time without LV<sub>i</sub> device--;

In column 7, line 20, replace "rate" with --gate--;  
line 29, replace "Particular low threshold variant of a Rate" with  
--particular low threshold variant of a gate--;

In column 8, line 11, remove the comma after "files".

Signed and Sealed this

Twenty-second Day of August, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*